

# The DataHub: Your Gateway to Effective and Efficient High-Performance Computing in Psychology and

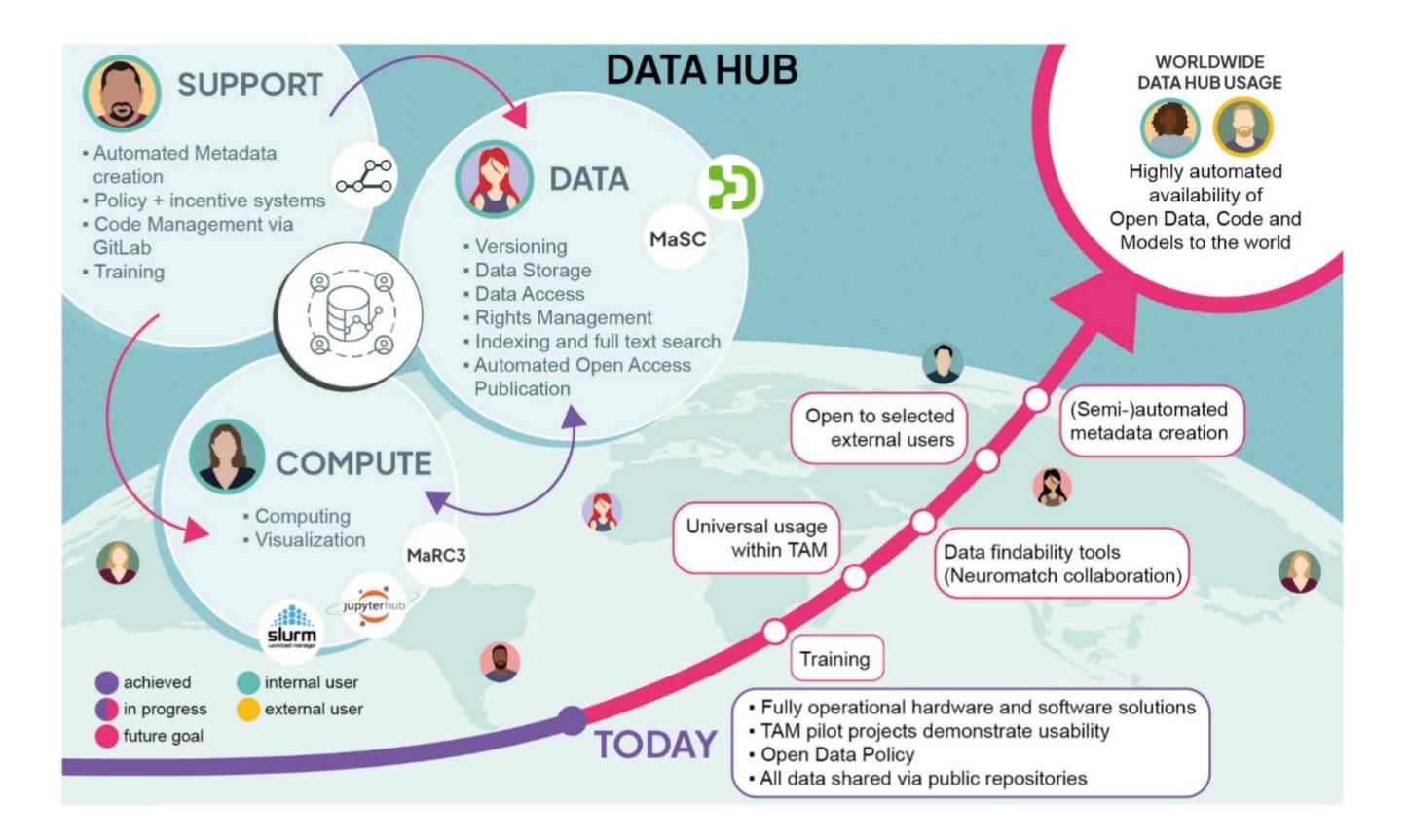
Neuroscience Research

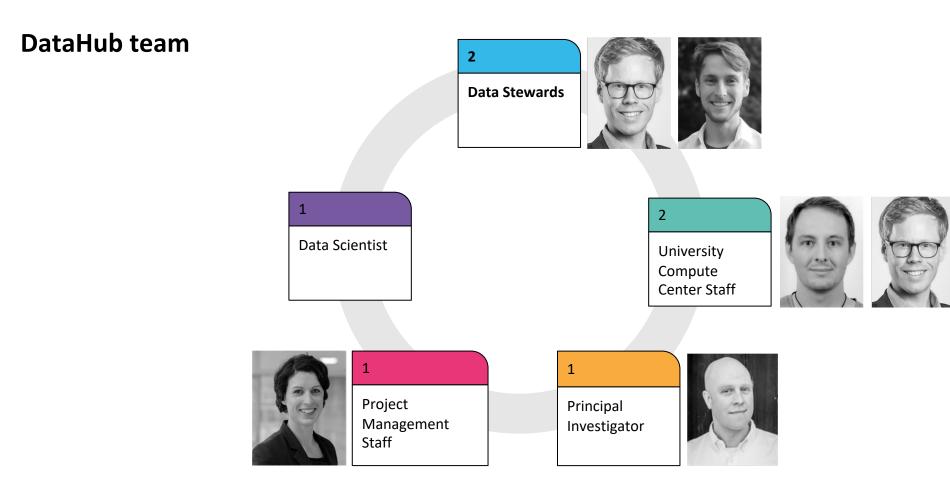


Nick Augustat, Stefan Lenze, Tobias Ortwein, Ortrun Brand, Bernd Nicklas, Dominik Endres Philipps-Universität Marburg



Vision, team & collaboration





#### Collaboration





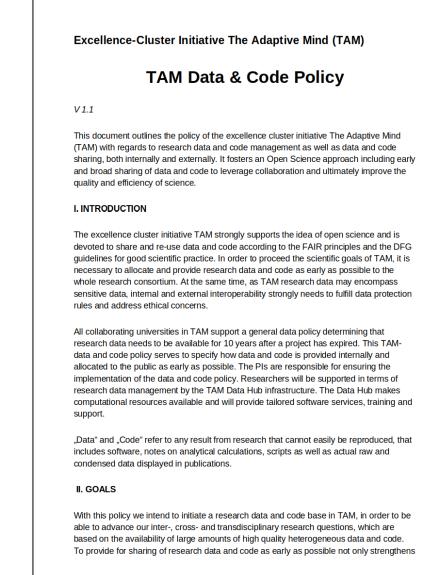








### Policy, training & support

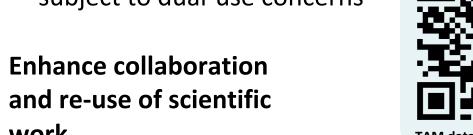


**Common workflow** 

#### **Principles of the TAM policy**

- Use shared hardware, services and
- support. Share data and code as early as possible.
- Practice FAIR principles and common standards.
- Make results more transparent and reproducible.
- Prepare for long-term accessibility and publish.
- To do: frictionless workflow for international students subject to dual-use concerns

work.





- Access & onboarding
- DataHub workflow
- Service descriptions
- Data & code management Git and GitLab tutorial
- Recorded workshops

FAQ & troubleshoot

DataHub

Repository

DSPACE

dataset

annotation &

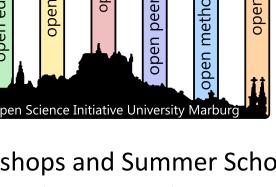
curation

archiving &

publishing

with DOI





Workshops and Summer Schools:

Fostering open science practices

Brainhack Summer School



Recorded tutorials, materials and instructions, websites and JupyterBooks on:

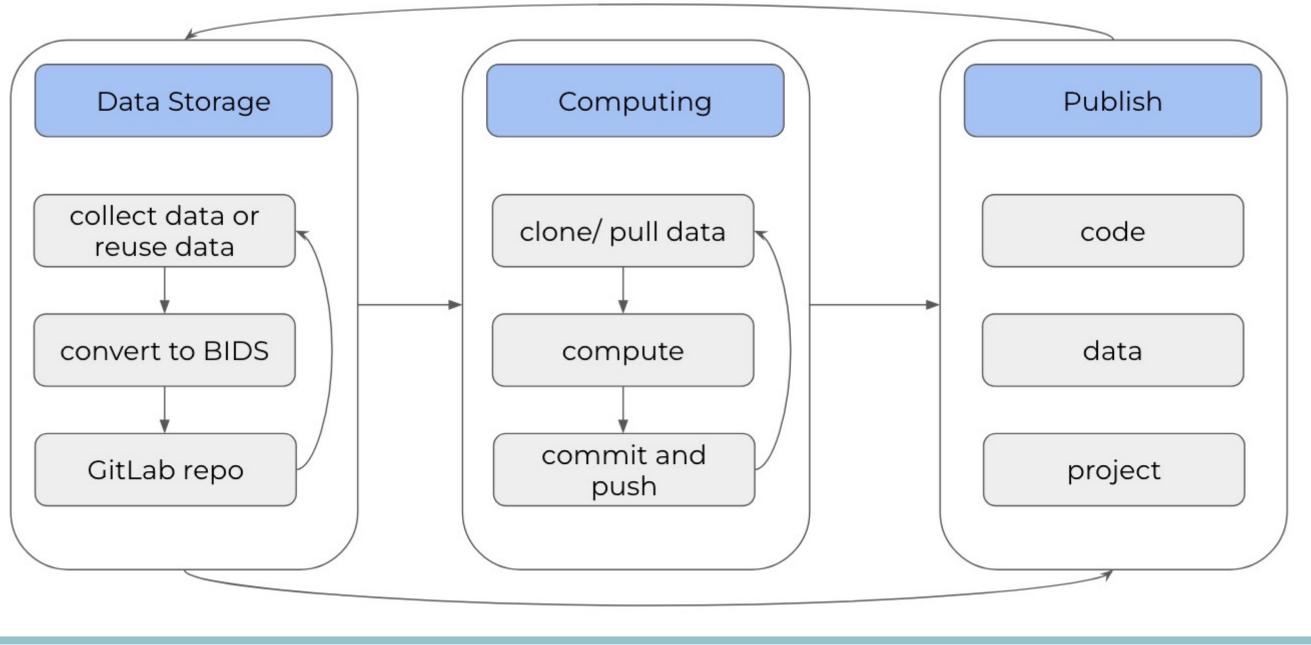
**Neuroscientific Workflow Assistance** 

SFB 135/NOWA

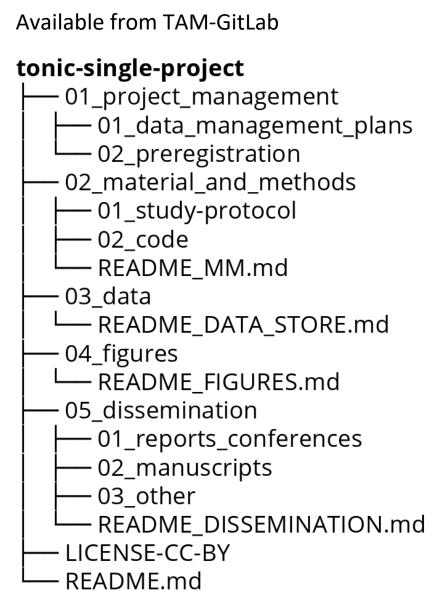
Git & GitLab,

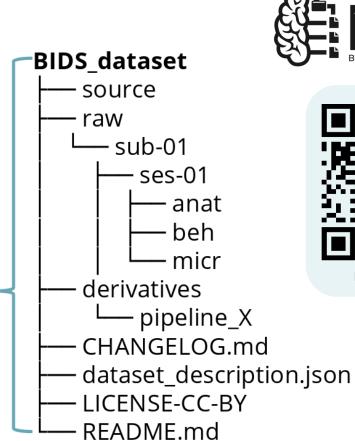
- Clean and reusable rode
- Continuous integration, code review, etc.

Workflows & standards

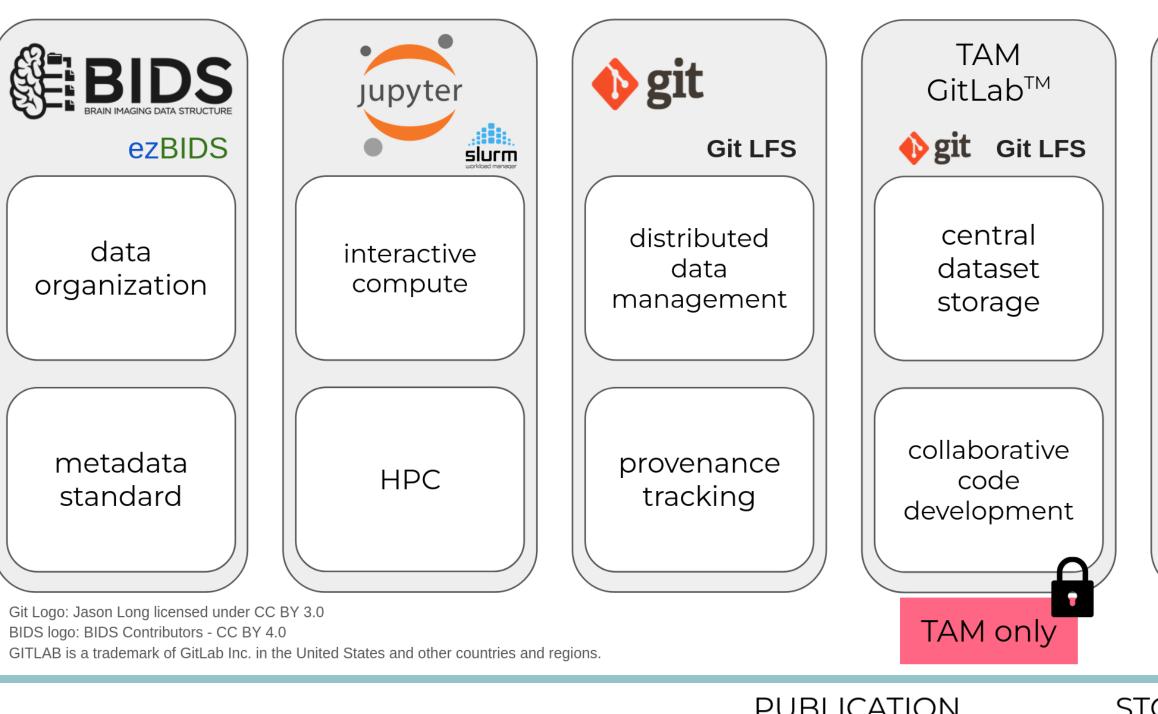


### **Tonic research folder template**





### Technical services



#### **DataHub status and latest features**

#### **Technical services**

• HPC integration: Production



• **TAM GitLab**: Production (advanced access management)

• Git + Git-LFS workflow integration: Production TAM DataHub repository: Pilot

GitLab - DSpace publication pipeline: Planned

• ezBIDS: Test

**Support services** User manual (GitLab pages): Production

• Support queue (request tracker): Production • Curation service (DSpace & GitLab): Pilot

Data protection policy

## Technical resources & architecture

### TAM nodes in MaRC3

MaRC3 total resources

3264 CPU cores

**→** 256-1024 GB RAM

6x A40, 4x L40S

34 TB home storage

(storage: 200 GBit)

25 GBit Ethernet network

51 compute nodes,

26x AMD EPYC 7702P, 64 cores, 2 GHz

2x AMD EPYC 9334, 2x32 cores, 2.7 GHz

\_\_ 12x V100S, 16x A100 40 GB, 70x A100 80 GB,

1.7-7.2 TB local, 134 TB shared scratch,

23x AMD EPYC 7713, 64 cores, 2 GHz

• 14 nodes / 896 cores + 4 GPUs (A100 80GB) 4 nodes / 192 cores (CPU-only)

#### TAM storage in **MaSC**

• 703 TB total usable capacity

